FUDA ELECTRONICS

LCD Module Specification

Model No.: AT070TN01

Date : May 2003

7 inch TFT

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1. General specifications

NO.	Item	Item Specification				
1	LCD size	7.0 inch				
2	Driver Element	a-Si TFT active matrix				
3	Display contents	480 pixels X 234 pixels				
4	Display Mode	Normally white, Transmissive with Backlight				
5	Dot pitch	0.107(W) X 0.370(H) mm				
6	Active area	154.08(W) X 86.58(H) mm				
7	Module Size	164.9 X 100 X 5.7 mm				
8	Surface Treatment	AG				
9	Weight	160 g Typical				

2. Electrical characteristics

(1). Absolute maximum ratings

Item	Symbol	Condition			Remark	
item	Symbol	Condition	Min.	Max.	Unit	Remark
	V _{CC}	GND=0	(-0.3)	(7)	٧	
	AV_{DD}	AVSS=0	(-0.3)	(7)	>	
Power voltage	V_{GH}	CND-0	(-0.3)	(18)	>	
	V_{GL}	GND=0	(-15)	(0.3)	V	
	V _{GH} -V _{GL}		-	(33)	V	
	Vi		(-0.3)	AV _{DD} +0.3	V	Note 1
Input signal voltage	VI		(-0.3)	V _{CC} +0.3	>	Note 2
	VCOM		(-2.9)	(5.2)	٧	
Operation Temperature (Ambient)	Тор		-10	60	$^{\circ}\!\mathbb{C}$	
Storage Temperature (Ambient)	Tst		-20	70	$^{\circ}\! \mathbb{C}$	

Note:

- 1. VR, VG, VB.
- 2. STHL, STHR, OEH, L/R, CPH1~CPH3, STVR, STVL, OEV, CKV, U/D.

(2). Pin assignment

(a). TFT LCD panel diving section

Pin no	Symbol	Ю	Function	Remark
1	GND	ı	Ground for logic circuit	
2	V _{cc}	I	Supply voltage of logic control circuit for scan driver	
3	V_{GL}	I	Negative power for scan driver	

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4	V_{GH}	ı	Positive power for scan driver	
5	STVR	I/O	Vertical start pulse	Note 1
6	STVL	I/O	Vertical start pulse	Note 1
7	CKV	I	Shift clock input for scan driver	
8	U/D	I	UP/DOWN scan control input	Note 1,2
9	OEV	ı	Output enable control for scan driver	
10	VCOM	I	Common electrode driving signal	
11	VCOM	I	Common electrode driving signal	
12	L/R	I	LEFT/RIGHT scan control	Note 1,2
13	MOD	I	Sequential sampling and simultaneous sampling setting	Note 3
14	OEH	ı	Output enable control for data driver	
15	STHL	I/O	Start pulse for horizontal scan line	Note 1
16	STHR	I/O	Start pulse for horizontal scan line	Note 1
17	СРН3	I	Sampling and shifting clock pulse for data driver	
18	CPH2	I	Sampling and shifting clock pulse for data driver	
19	CPH1	I	Sampling and shifting clock pulse for data driver	
20	V _{cc}	I	Supply voltage of logic control circuit for data driver	
21	GND	-	Ground for logic circuit	
22	VR	I	Alternated video signal (Red)	
23	VG	I	Alternated video signal (Green)	
24	VB	I	Alternated video signal (Blue)	
25	AV_DD	I	Supply voltage for analog circuit	
26	AV_SS	-	Ground for analog circuit	

Note:

1. Selection of scanning mode (please refer to the following table)

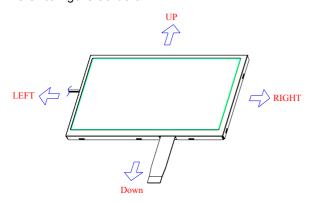
	of scan of input	IN/C	OUT state	for start p	oulse	Scanning direction		
U/D	L/R	STVR	STVL	STHR	STHL	Č		
GND	V _{CC}	0	I	0	I	Up to down, left to right		
V _{CC}	GND	I	0	I	0	Down to up, right to left		
GND	GND	0	I	I	0	Up to down, right to left		
V _{CC}	V _{cc}	I	0	0	I	Down to up, left to right		

I: input, O: output

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2. Definition of Scanning Direction.

Refer to figure as below:



3. MOD=H: Simultaneous sampling.

MOD=L: Sequential sampling.

Please set CPH2 and CPH3 to GND when MOD=H,

(b).Backlight unit

Pin no	Symbol	Function	Remark
1	HI	Power supply for backlight unit (high voltage)	Pink
2	GND	Ground for backlight unit	White

(3). Electrical characteristics

(a). Typical operating conditions (GND=AV_{SS}=0V, Note 4)

				Values			Remark
Ite	m	Symbol	Min.	Тур.	Max.	Unit	Remark
		V _{CC}	3	5	5.2	٧	
Dames		AV_DD	4.8	5	5.2	V	
Power	supply	V_{GH}	14.3	15	15.7	٧	
		V_{GH}	-10.5	-10	-9.5	>	
		V_{iA}	0.4	-	AV _{DD} -0.4	V	Note1
Video signal a VG,		V _{iAC}	-	3	-	V	AC component
V 0,	V D)	V_{iDC}	-	AV _{DD} /2	-	V	DC component
\/O(214	V_{CAC}	3.5	5.6	6.5	V	Note2
VCOM		V_{CDC}	1.7	2.0	2.3	V	DC component
Input signal H level		V _{IH}	0.8V _{CC}	-	V _{CC}	V	Nata 2
Voltage	L level	V _{IL}	0	-	0.2V _{CC}	V	Note3

Note:

- 1. Refer to Fig.3-(a).
- 2. The brightness of LCD panel could be changed by adjusting the AC component of VCOM.
- 3. SRHL, STHR, OEH, L/R, CPH1~CPH3, STVR, STVL, OEV, CKV, U/D
- 4. Be sure to apply GND, V_{CC} , and V_{GL} , to the LCD first, and then apply V_{GH}

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(b). Current consumption (GND=AV_{SS}=0V)

			Values			Remark	
Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
	I _{GH}	V _{GH} =15V		0.2	0.5	mA	
O	I_{GL}	V _{GL} =-10V		0.8	1.5	mA	
Current for Driver	I _{cc}	V _{CC} =5V		3.0	6.0	mA	
	I _{DD}	AV _{DD} =5V		17	30	mA	

(c). Backlight driving conditions

		Values				Remark
Item	Symbol	Min.	Тур.	Max.	Unit	Kemark
Lamp voltage	V_L	-	560	620	Vrms	
Lamp Current	IL	-	6	7	mArms	
Frequency	FL	-	60	80	kHz	Note 4
		-	-	900	Vrms	Note 1,5
Lamp starting voltage	Vs	-	-	-	Vrms	Note 2,5
		-	-	1,100	Vrms	Note 3,5
Lamp life time		10,000	-	=	Hr	Note 6

Note:

- 1. Ta=25°C
- 2. Ta=0°C
- 3. Ta=-20°C
- 4. The lamp frequency should be selected as different as possible from display horizontal synchronous signal to avoid interference
- 5. For starting the backlight unit, the output voltage of DC/AC's transformer should be larger than the maximum lamp starting voltage.

(4). AC timing

(a). Timing conditions (sequential mode)

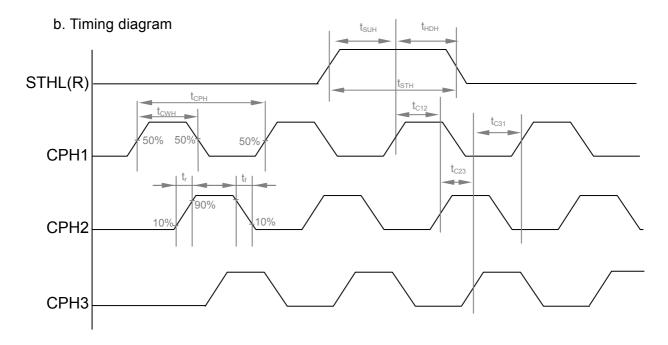
		Values				Remark
Item	Symbol	Min.	Тур.	Max.	Unit	Nemark
Rising time	t _r	-	-	10	ns	Note 1
Falling time	t _f	-	-	10	ns	Note 1
High and low level pulse width	t _{CPH}	99	103	107	ns	CPH1~CPH3
CPH pulse duty	t_{CWH}	40	50	60	%	CPH1~CPH3
CPH pulse delay	t _{C12} t _{C23} t _{C31}	30	t _{CPH} /3	t _{CPH} /2	ns	CPH1~CPH3

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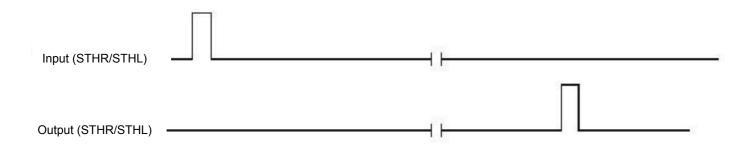
STH setup time	t _{suh}	20	-	-	ns	STHR, STHL
STH hold time	t_{HDH}	20	-	-	ns	STHR, STHL
STH pulse width	t_{STH}	-	1	ı	t _{CPH}	STHR, STHL
STH period	t _H	61.5	63.5	65.5	μ s	STHR, STHL
OEH pulse width	t _{OEH}	-	1.22	-	μ s	
Sample and hold disable time	t _{DIS1}	-	8.28		μ s	
OEV pulse width	t _{OEV}	-	5.40		μ s	
CKV pulse width	t _{CKV}	-	4.18	-	μ s	
Clean enable time	t _{DIS2}	-	3.74		μ s	
Horizontal display start	t _{SH}	-	0	-	t _{CPH} /3	
Horizontal display timing range	t _{DH}	-	1440	-	t _{CPH} /3	
STV setup time	t _{suv}	400	-	-	ns	STVL, STVR
STV hold time	t_{HDV}	400	-	ı	ns	STVL, STVR
STV pulse width	t_{STV}	-	-	1	t _H	STVL, STVR
Horizontal lines per field	t _V	256	262	268	t _H	Note 2
Vertical display start	t _{sv}		3	-	t _H	
Vertical display timing range	t_{DV}		234	-	t _H	
VCOM rising time	t _{rCOM}		-	5	μ S	
VCOM falling time	t _{fCOM}		-	5	μS	
VCOM delay time	t _{DCOM}		-	3	μs	
RGB delay time	t _{DRGB}		-	1	μS	

Note:

- 1. For all of the logic signals
- 2. Please don't use odd horizontal lines to drive LCD panel for both odd and even field simultaneously.



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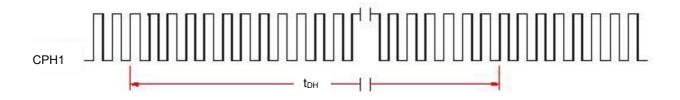


Fig.2 Sampling clock timing

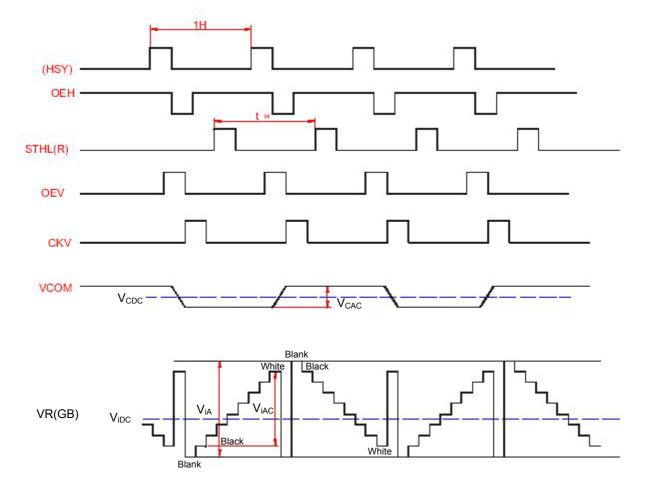
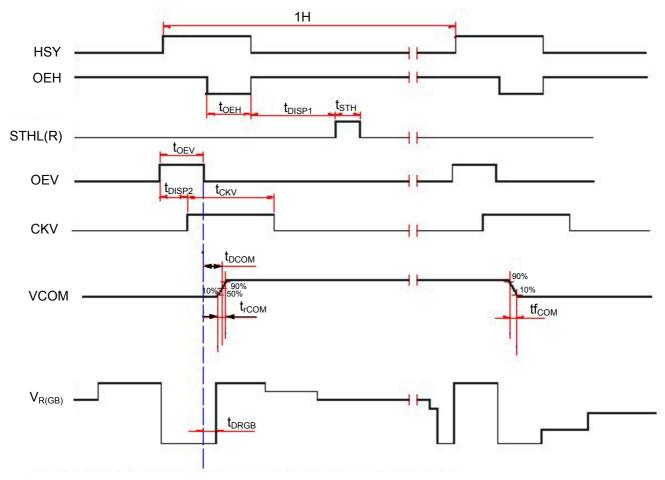


Fig.3-(a) Horizontal timing

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Note: The falling edge of OEV should be synchronized with the falling edge of OEH

Fig.3-(b) Detail horizontal timing

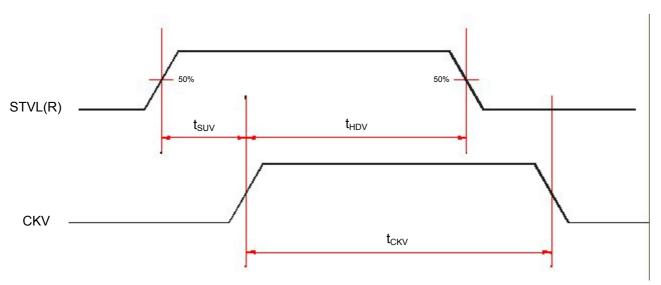
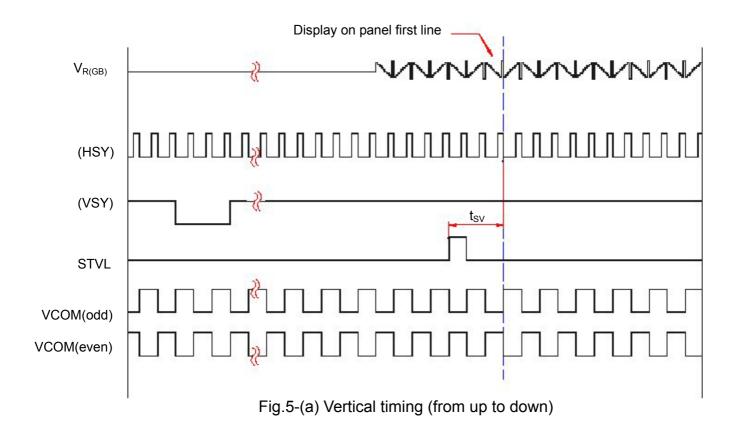


Fig.4 Vertical shift clock timing

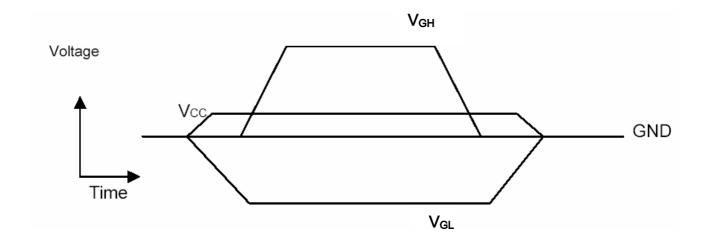
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(5) Power sequence

This module adopts high voltage driver IC, so it may be damaged by a large current flow if a wrong power on/off sequence is used! The recommend power sequence is to connect V_{CC} first, then connect power to driver gate power, V_{GL} and V_{GH} . When shutting off the power, shut off the driver gate power, V_{GL} and V_{GH} , then shut off the logic power, V_{CC} , or shut off the power simultaneously!



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3. Optical specifications

The following items are measured under stable conditions. The optical characteristics should be measured in dark room or equivalent state with the methods shown in Note 1.

Ta=25±2°C, I_L=6mA

Item		Symbol	Condition	Min	Тур	Max	Unit	Remark
Response time		T _R	0-0	-	10	50	ms	Note 2
		T _F	Θ=0	-	20	60	ms	Note2
Contrast r	atio	CR	At optimized viewing angle	200	300			Note3
Brightne	SS	YL	Θ=0	150	200		Cd/m ²	Note4
Color	\//b:4 a	W _x	0-0	0.26	0.31	0.36		Note 4
Chromaticity	White	W_{y}	Θ=0	0.28	0.33	0.38		Note4
	Hor.	Θ_{R}		50	60			
Viewing	пот.	ΘL	CR≥10	50	60		Degree	Note5
Angle	Ver.	Фн		30	40			
	VEI.	Фь		50	60	_		

Note:

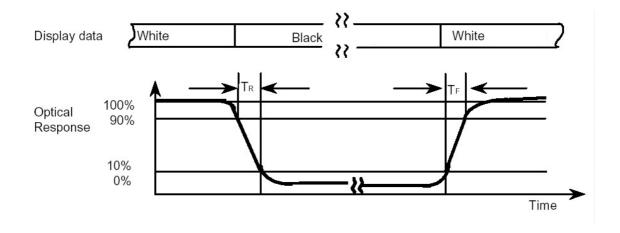
1. Test equipment setup

After stabilizing and leaving the panel alone at a given temperature for 30 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-5A with a viewing angle of

1° at a distance 0f 50cm and normal direction.

2. Definition of response time: T_R and T_F

The figure below is the output signal of the photo detector.



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3. Definition of contrast ratio:

White Vi = $Vi_{50\%} \pm 1.5 \text{ V}$

Black Vi = $Vi_{50\%} \mp 2.0 \text{ V}$

"±"means that the analog input signal swings in phase with Vcom signal.

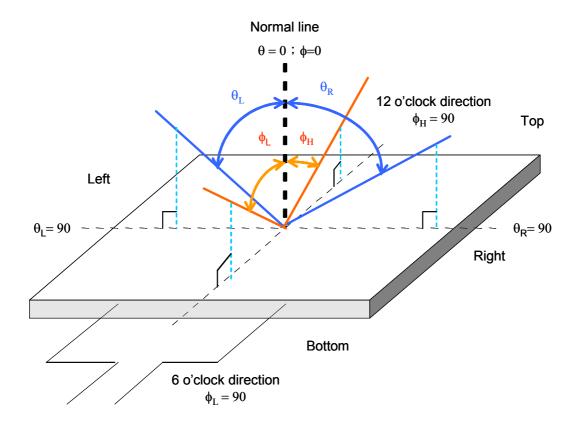
"+" means that the analog input signal swings out of phase with Vcom signal.

V_{150%}: The analog input voltage when transmission is 50%

The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.

4. Measured at the center area of the panel when all the input terminal of LCD panel are electrically opened.

5. Definition of viewing angle:



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4. Reliability test items

(1). Reliability levels in mass production are as below:

Test Items	Test Conditions
High temperature storage	+70°C±3°C, Dry(30%RH max.) For 240 hours
Low temperature storage	-20°C±3°C for 240 hours
High temperature operation	+60°C±3°C, Dry(30%RH max.) for 240 hours
Low temperature operation	-10°C±3°C for 240 hours
Operation at high	+40°C±3°C,90%±3%RH max. for 240 hours
temperature and humidity	
Thermal shock	-20degree/0.5h ~ +70 degree/0.5h for a total 20 cycles
Mechanical shock	Drop onto the tilted floor from 60cm heights, 1 corner, 3 edges, 6 faces.
	Apply shipping package to this test
Vibration test	Sweep at 10Hz to 55Hz to 10Hz, amplitude 0.75mm for 20cycles each in X,Y
	and Z directions.
	Apply shipping package to this test.

Note1: High temp storage & High temp/High humidity Op the polarizer is out of subject

Note 2: the test sample has recovery time 2 hours at room temp before function check

5. Handling precautions

1 Safety

The liquid crystal in the LCD is poisonous. **DO NOT** put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.

2 Handling

- 1) The LCD panel is plate glass. **DO NOT** subject the panel to mechanical shock or to excessive force on its surface.
- 2) The polarizer attached to the display is very easy to damage, handle it with careful attention.
- 3) To avoid contamination on the display surface, **DO NOT** touch the display surface with bare hands.
- 4) Provide a space so that the LCD panel does not come into contact with other components.
- 5) To protect the LCD panel from external pressure, put covering glass (acrylic board or similar board) keeping appropriate gap between them.
- 6) Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where dew condensation occurs.
- 7) Property of semiconductor devices may be affected when they are exposed to light, possibly resulting in malfunctioning of the ICs.
- 8)To prevent such malfunctioning of the ICs, your design and mounting layout done are so that the IC is not exposed to light in actual use.

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3 Static electricity

- 1) Ground soldering iron tips, tools and testers when you operate.
- 2) Ground your body when handling the products.
- 3) **DO NOT** apply voltage to the input terminal without applying power supply.
- 4) **DO NOT** apply voltage which exceeds the absolute maximum rating.
- 5) Store the products in an anti-electrostatic container.

4 Storage

- 1) Store the products in a dark place at +25°C±10°C, low humidity (65%RH or less).
- 2) **DO NOT** store the products in an atmosphere containing organic solvents or corrosive gases.

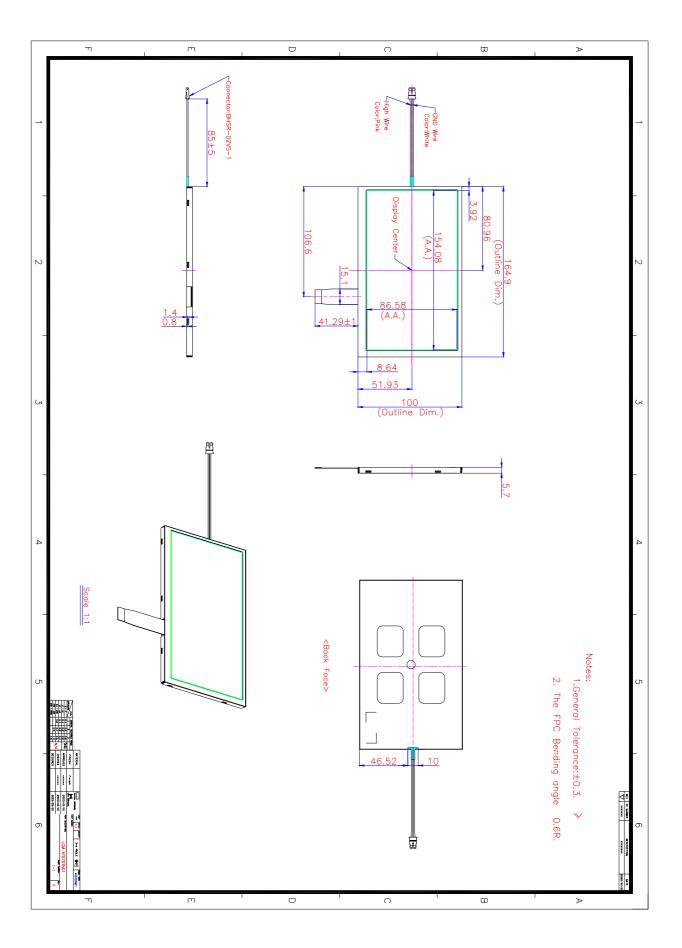
5Cleaning

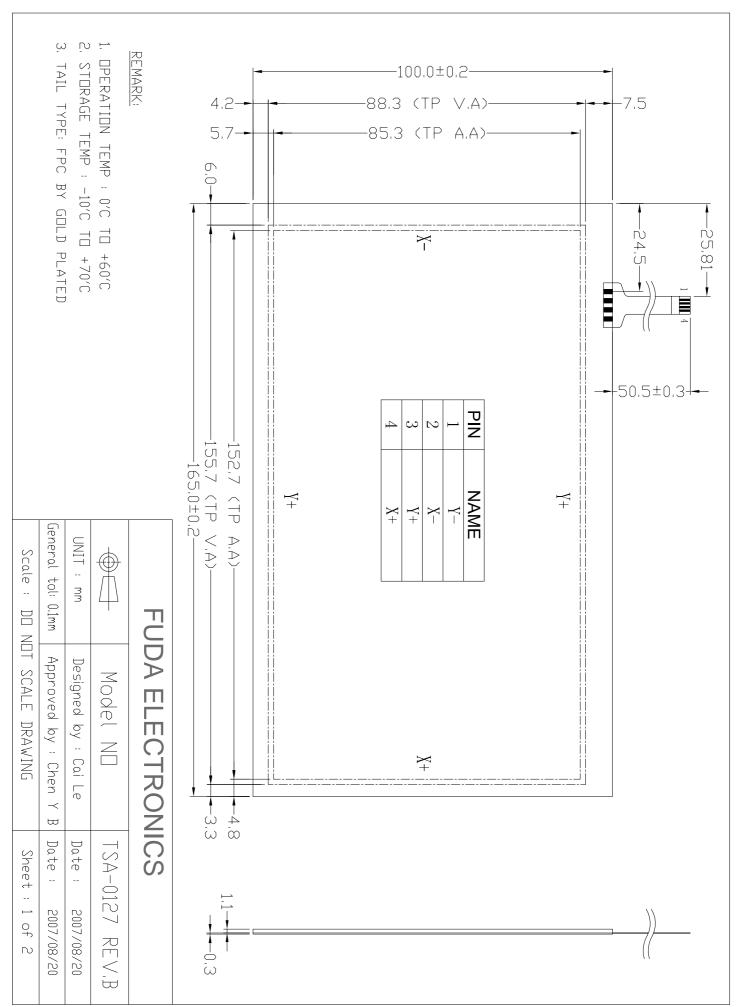
- 1) **DO NOT** wipe the polarizer with dry cloth, as it might cause scratch.
- 2) Wipe the polarizer with a soft cloth soaked with petroleum IPA, other chemical might damage.

6. Mechanical dimensions



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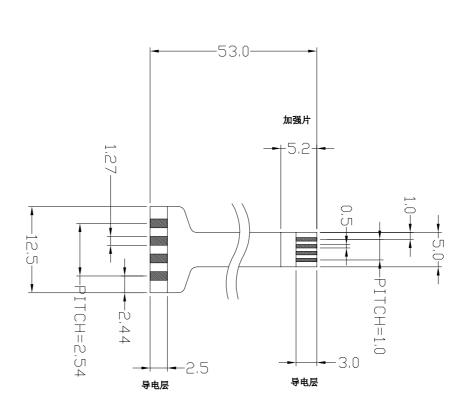






REMARK:

- 3. TAIL TYPE: FPC BY GOLD PLATED



Sheet : 1 of 1	Scale : DO NOT SCALE DRAWING	Scale : DO N
Date : 2007/08/	Approved by : Chen Y B Date :	General tol: 0.1mm
Date : 2007/08/	Designed by : Cai Le	UNIT : mm
TSA-0127 FP	Model NO	
NICS	FUDA ELECTRONICS	FI

0127 FPC

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导电层		导电景
	<u> </u>	
	0.1	\ ω
早电层		加强片

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7. Packing specifications

(1). Packaging material table

Per carton

No	Item	Model (Material)	Dimensions (mm)	Unit Weight (Kg)	Quantity	Remark
1	LCM module	AT070TN01	164.9*100*5.7	0.160	30	
2	EPP tray	EPP	516*384*6.5	0.07	7	Anti-static
3	Carton	Carton	530*355*255	1.06	1	
4	Total weight	6.5 Kg ± 5%				

(2). Packaging quantity

(1) LCM quantity per tray: no. of the row	5 x no. the column 6 = 30
(2) Total LCM quantity in Carton: no. of EPP trays	30 x quantity per tray 7= 30