Ver.: 1.0

MODEL NO. : TS070OAAAD01

ISSUED DATE: <u>2007-8-20</u>

VERSION : <u>Ver 1.0</u>

■ Preliminary Product Specification

☐ Final Product Specification

#### **SHANGHAI TIANMA Confirmed:**

Prepared by	Prepared by Checked by				
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2007-08-20	2007-08-20	2007-08-27			

This technical specification is subjected to change without notice





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#### **Record of Revision**

Rev	Issued Date	Description
0.10	Aug,20, 2007	Preliminary release

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# 1. GENERAL SPECIFICATIONS

	Feature	Spec	
	Size	7"	
	Resolution	480*RGB*234	
	Interface	Analog RGB	
	Technology type	α-Si TFT	
Display Spec.	Pixel pitch (mm)	0.321*0.370	
Display Spec.	Display colors	Full color	
	Pixel Configuration	RGB vertical stripe	
	Display Mode	TM , NW	
	Surface Treatment	Anti-Glare	
	Gray Scale Inversion	6 o'clock	
	LCM (W x H x D) (mm)	164.9*100*5.7	
   Mechanical	Active Area(mm)	154.08*86.58	
Characteristics	With /Without TSP	Without TSP	
Citaracteristics	Weight (gram)	TBD.	
	LED Numbers	27 LEDs	

Note 1: Requirements on Environmental Protection: RoHS

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# 2. ABSOLUTE MAXIMUM RATINGS

GND=0V, Ta = 25°C

Item	Symbol	MIN	MAX	Unit	Remark
	VCC	-0.3	7.0	V	
	AVDD	-0.3	7.0	V	
Power Voltage	VGH	-0.3	18.0	V	
	VGL	-15.0	0.3	V	
	VGH-VGL	-	33.0	V	
Input cianal voltage	Vı	-0.2	AVDD+0.2	V	Note 1
Input signal voltage	$V_L$	-0.3	AVDD+0.3	V	Note 2
Operating Temperature	Тор	-20	70	$^{\circ}\!$	
Storage Temperature	Tst	-30	80	$^{\circ}\!\mathbb{C}$	

**Table 2.1 Absolute maximum rating** 

Note 1: VR, VG, VB

Note 2: STHL, STHR, OEH, L/R, CPH1-3, STVL, STVR, OEV, CKV, U/D

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# 3. ELECTRICAL CHARACTERISTICS

#### 3.1 LCD module

GND=0V,Ta=25°C

Item		Symbol	MIN	TYP	MAX	Unit	Remark
Logic supply voltage		VCC	4.8	5.0	5.2	V	
Analog supply v	oltage	AVDD	4.8	5.0	5.2	V	
Negative power	for scan driver	VGL	-10.5	-10.0	-9.5	V	
Positive power for scan driver		VGH	14.3	15.0	15.7	V	
Input Signal	Low Level	VIL	0	-	0.3VCC	V	-Note 1
Voltage	High Level	VIH	0.7VCC	-	VCC	V	- Note 1
		V <sub>IA</sub>	0.2	-	AVDD-0.2	V	Note 2
Video Signal Am	plitude	V <sub>IAC</sub>	-	3	-	V	Note 3
		V <sub>IDC</sub>	-	AVDD/2	-	V	
VCOM		$V_{CAC}$	3.5	5.6	6.5	V	
VCOIVI		V <sub>CDC</sub>	1.55	-	1.95	V	
		I <sub>VCC</sub>	-	TBD	-	mA	
Dawer Cons	I <sub>AVDD</sub>	-	TBD	-	mA		
Power Consumption		I <sub>VGH</sub>	-	TBD	-	mA	
				TBD	-	mA	

## Table 3.1 LCD module electrical characteristics

Note 1: STHL, STHR, OEH, L/R, CPH1-3, STVL, STVR, OEV, CKV, U/D

Note 2: the amplitude of VR,VG,VB blank to blank.

Note 3: the amplitude of VR, VG, VB black to white.

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## 3.2 Backlight Unit

Ta=25°C

ltem	Symbol	MIN	TYP	MAX	Unit	Remark
Forward Current	I <sub>F</sub>	-	180	-	mA	
Forward Current Voltage	V <sub>F</sub>	-	9.6	-	V	
Backlight Power Consumption	W <sub>BL</sub>	-	1728	-	mW	

**Table 3.2 Backlight unit electrical characteristics** 

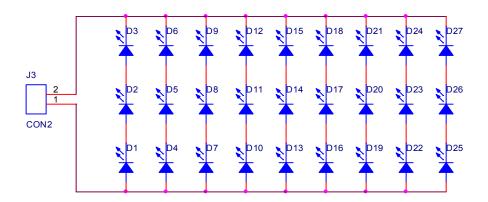


Figure 3.1 LED driver circuit

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# 4. BLOCK DIAGRAM

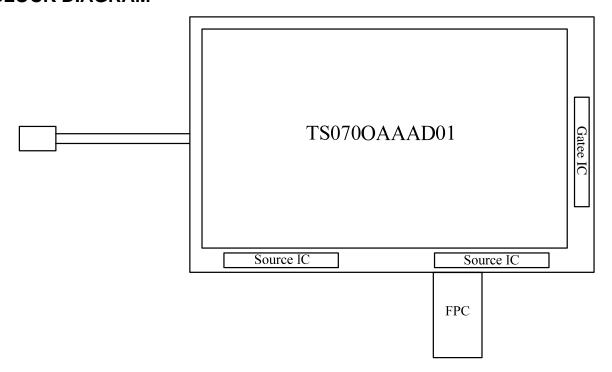


Figure 4.1 LCD module diagram

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# 5. INPUT TERMINALS PIN ASSIGNMENT

## 5.1 TFT LCD panel driving section

No	Symbol	I/O	Description	Remark
1	GND	Р	Ground	
2	VCC	Р	Supply voltage for scan driver	
3	VGL	Р	Negative power for scan driver	
4	VGH	Р	Positive power for scan driver	
5	STVD	I/O	Vertical start pulse down side	Note 1
6	STVU	I/O	Vertical start pulse up side	Note 1
7	CKV	I	Shift clock input	
8	U/D	I	UP/DOWN scan control input	Note 1
9	OEV	I	Output enable control for scan	
10	VCOM	I	Common electrode driving signal	
11	VCOM	I	Common electrode driving signal	
12	L/R	I	LEFT/RIGHT scan control input	Note 1
13	MOD	I	Sequential sampling and simultaneous sampling setting	Note 2
14	OEH	I	Output enable control for data driver	
15	STHL	I/O	Start pulse for horizontal scan line left side	Note 1
16	STHR	I/O	Start pulse for horizontal scan line right side	Note 1
17	CPH3	I	Sampling and shifting clock pulse for data driver	Note 2
18	CPH2	I	Sampling and shifting clock pulse for data driver	Note 2
19	CPH1	I	Sampling and shifting clock pulse for data driver	Note 2
20	VCC	Р	Supply voltage for data driver	
21	GND	Р	Ground	
22	VR	I	Alternated video signal(Red)	
23	VG	I	Alternated video signal(Green)	
24	VB	I	Alternated video signal(Blue)	
25	AVDD	Р	Supply voltage for analog circuit	
26	AVSS	Р	Ground for analog circuit	

Table 5.1 Input terminal pin assignment

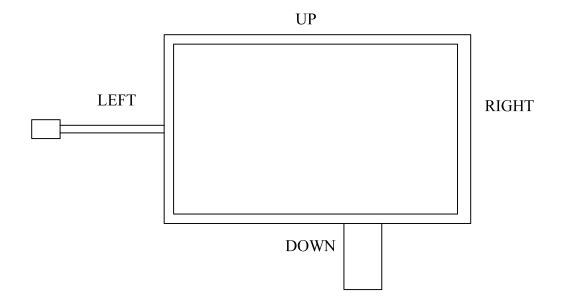
P: Power/GND; I: input pin; I/O: input/output pin;

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Note 1: select of scanning mode

Setting of sca	In/out sta	ate for st	art pulse	Soonning direction		
U/D	L/R	STVD	STVU	STHR	STHL	Scanning direction
GND	VCC	0	1	0	1	Up to down, left to right
VCC	GND	1	0	I	0	Down to up, right to left
GND	GND	0	1	1	0	Up to down, right to left
VCC	VCC	I	0	0	1	Down to up, left to right

Refer to the figure as below



Note 2: MOD=H, simultaneous sampling.

MOD=L, sequential sampling.

Please set CPH2 and CPH3 to GND when MOD=H.

## 5.2 Backlight unit section

Pin No.	Symbol	I/O	Function	Remark
1	HI	Р	Power supply for backlight unit	Pink
2	GND	Р	Ground for backlight unit	White

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## 6. INTERFACE TIMING

## **6.1 Timing Parameter**

Parameter	Symbol	Min.	Тур.	Max.	Unit.	Remark
Rising time	t <sub>r</sub>	-	-	60	ns	Note 1
Falling time	t <sub>f</sub>	-	-	60	ns	Note 1
High and low level pulse width	t <sub>CPH</sub>	150	154	158	ns	CPH1-3
CPH pulse width	t <sub>CWH</sub>	40	50	60	ns	CPH1-3
	t <sub>12</sub>					
CPH pulse delay	t <sub>23</sub>	30	t <sub>CPH</sub> /3	t <sub>CPH</sub> /2	ns	CPH1-3
	t <sub>31</sub>					
STH setup time	t <sub>SUH</sub>	20	-	-	ns	STHL/R
STH hold time	t <sub>HDH</sub>	20	-	-	ns	STHL/R
STH pulse width	t <sub>STH</sub>	-	1	-	t <sub>CPH</sub>	STHL/R
STH period	t <sub>H</sub>	61.5	63.5	65.5	us	STHL/R
OEH pulse width	t <sub>OEH</sub>	-	7	-	us	
Sample and hold disable time	t <sub>DIS1</sub>	-	55	-	us	
OEV pulse width	t <sub>OEV</sub>	-	27	-	us	
CKV pulse width	t <sub>CKV</sub>	16	-	40	us	
Clean enable time	t <sub>DIS2</sub>	-	16	-	us	
Horizontal display time range	t <sub>DH</sub>	-	960	-	t <sub>CPH</sub>	
STV setup time	t <sub>SUV</sub>	400	-	-	ns	STVD/U
STV hold time	t <sub>HDV</sub>	400	-	-	ns	STVD/U
STV pulse width	t <sub>STV</sub>	-	-	1	t <sub>H</sub>	STVD/U
Horizontal line per field	t <sub>V</sub>	256	262.5	268	t <sub>H</sub>	Note 2
Vertical display start	t <sub>SV</sub>	-	3	-	t <sub>H</sub>	
Vertical display range	t <sub>DV</sub>	-	234	-	t <sub>H</sub>	
VCOM rising time	$t_{rCOM}$	-	-	5	us	
VCOM falling time	t <sub>fCOM</sub>	-	-	5	us	
VCOM delay time	t <sub>DCOM</sub>	-	-	3	us	
RGB delay time	t <sub>DRGB</sub>	-	-	1	us	

Note 1: For all of logic signal.

Note 2: Please don't use odd horizontal lines to drive LCD panel for both odd and even field simultaneously.

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## **6.2 Timing Diagram**

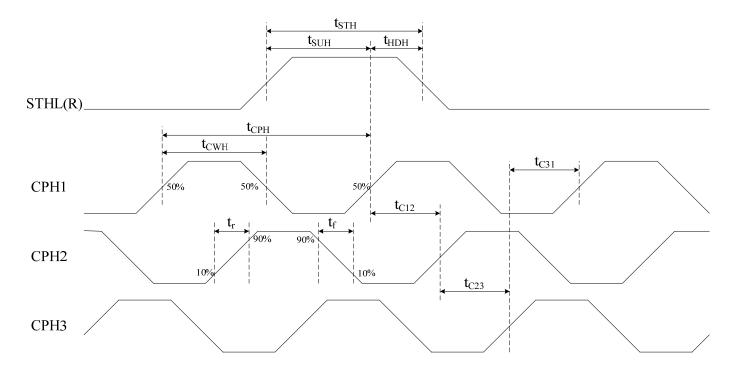


Figure 6.1: Sampling clock timing

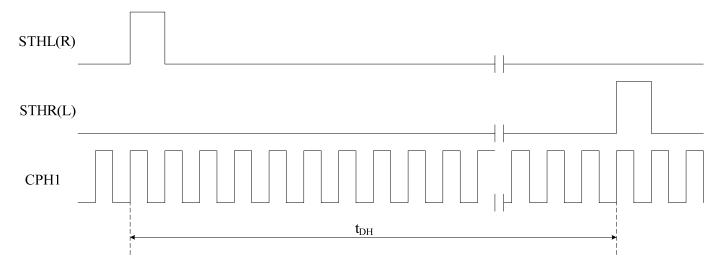


Figure 6.2: Horizontal display range timing

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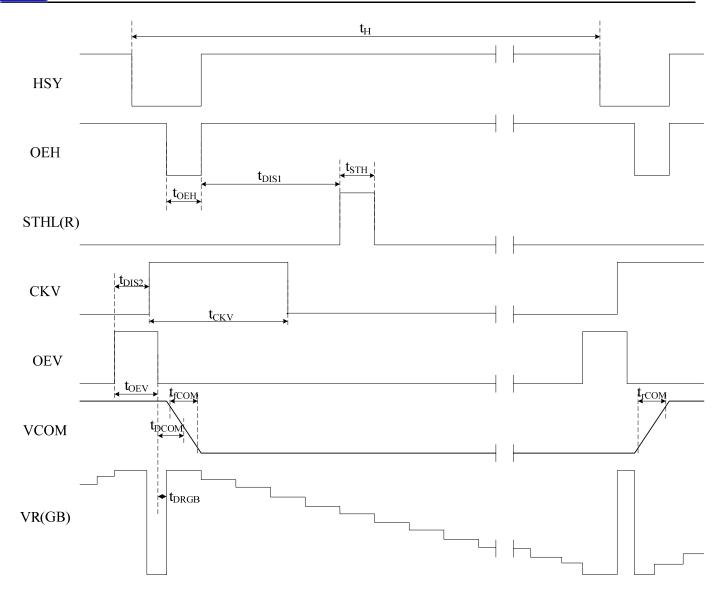


Figure 6.3: Horizontal display timing

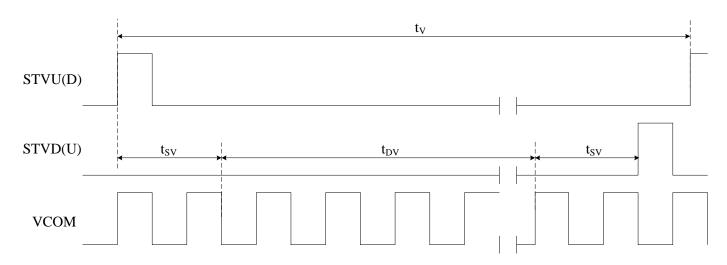


Figure 6.4: Vertical display timing

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# 7. POWER ON/OFF SEQUENCE

## 7.1 Power on Sequence

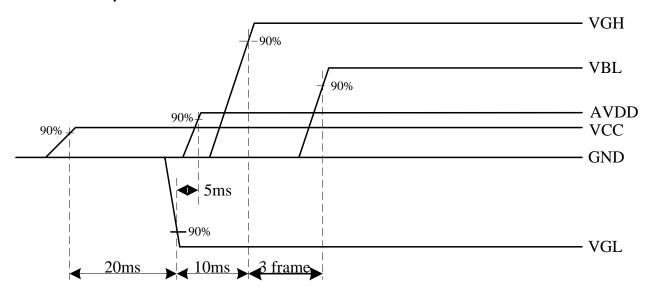


Figure 7.1 Power on sequence

Note: the interval time should more than the label

## 7.2 Power off Sequence

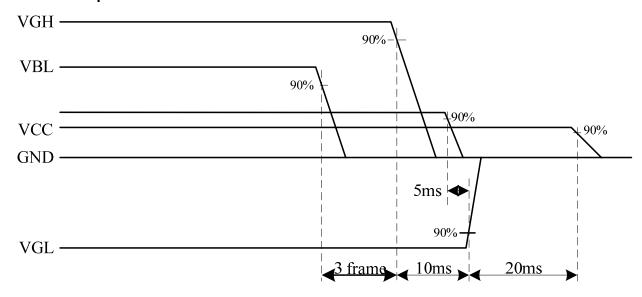


Figure 7.2 Power off sequence

Note: the interval time should more than the label



# 8. OPTICAL CHARACTERISTICS

Ta=25°C

Item		Symbol	Condition	Min	Тур.	Max.	Unit	Remark
View Angles		θL		60	65			
		$\Theta$ R	CR≧10	60	65		Dagge	Nata 2.2
		θТ	CK≦10	40	45		Degree	Note2,3
		$\theta$ B		60	65			
Contrast I	Ratio	CR	<i>θ</i> =0°	300	400			Note 3
Response	Timo	Tr	<b>2</b> 5℃		25	50	ma	Note 4
Kesponse	Time	Tf	23 (		25	30	ms	Note 4
	White	X		0.260	0.310	0.360		
	vviiite	y		0.280	0.330	0.380		
	RED	x x		0.535	0.585	0.635		
Chromaticity		y	Backlight	0.310	0.360	0.410		Note 1,5
Cilioniaticity	GREEN	X	on	0.285	0.335	0.385		Note 1,5
	GKEEN	y		0.530	0.580	0.630		
	BLUE	X		0.095	0.145	0.195		
	DLOE	y		0.065	0.115	0.165		
Uniform	ity	U		70	75		%	Note 6
NTSC		(x,y)			50		%	Note 5
Lumina	nce	L		280	350		cd/m <sup>2</sup>	Note 7

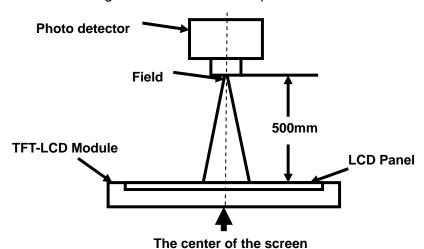
#### Test Conditions:

- 1. The ambient temperature is  $25^{\circ}$ C.
- 2. The test systems refer to Note 1 and Note 2.

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## Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Item	Photo detector	Field
Contrast Ratio		1°
Luminance	SR-3	
Chromaticity	3K-3	
Lum Uniformity		
Response Time	BM-7A	

Note 2: Definition of viewing angle range and measurement system.

viewing angle is measured at the center point of the LCD by conoscope.

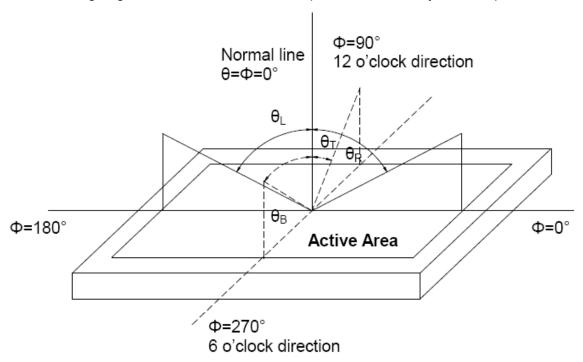


Figure 8. 1 Definition of viewing angle

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## Note 3: Definition of contrast ratio

Contrast ratio (CR) = Luminance measured when LCD is on the "White" state

Luminance measured when LCD is on the "Black" state

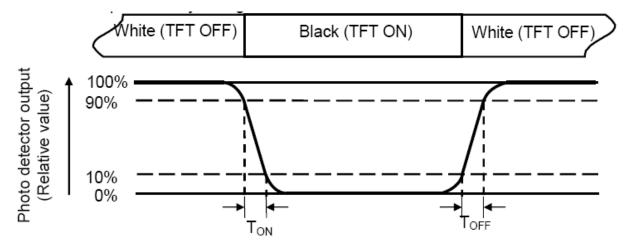
"White state ":The state is which the LCD is driven by Vwhite.

"Black state": The state is which the LCD is driven by Vblack.

**V**white: To be determined **V**black: To be determined.

#### Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931) Color coordinates measured at center point of LCD.

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Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity(U) = Lmin/Lmax

L-----Active area length W----- Active area width

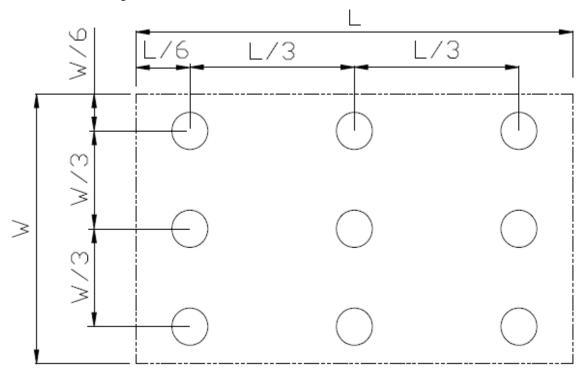


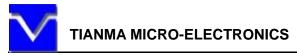
Figure 8.2 Definition of uniformity

Lmax: The measured maximum luminance of all measurement position.

Lmin: The measured minimum luminance of all measurement position.

# Note 7: Definition of Luminance:

Measure the luminance of white state at center point



# 9. Environmental / Reliability Tests

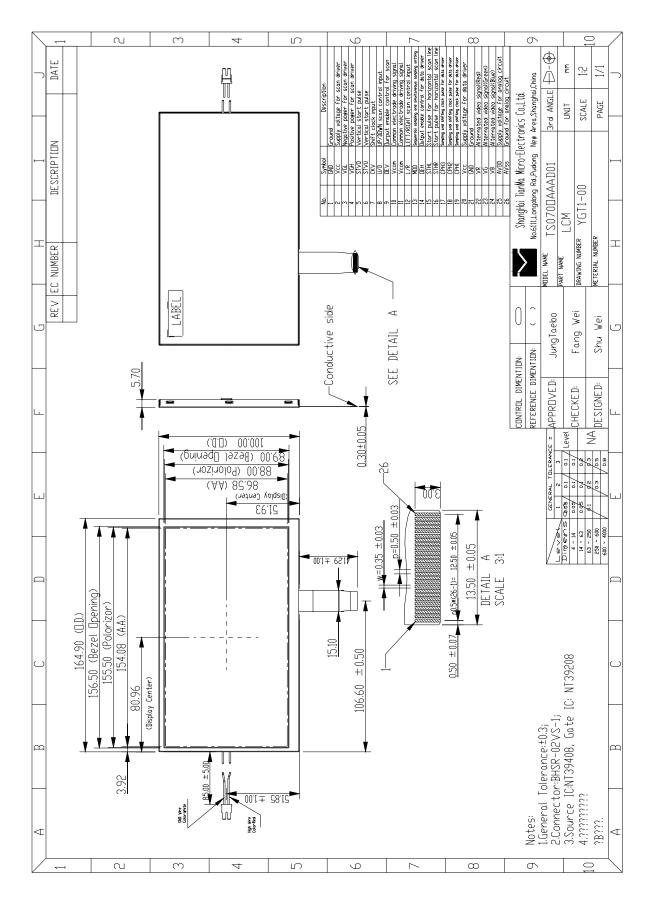
No	Test Item	Condition	Remarks
1	High Temperature	Ts=+70℃, 240hrs	Note1
	Operation		IEC60068-2-2,GB2423.2—89
2	Low Temperature	Ta=-20°ℂ , 240hrs	IEC60068-2-1
	Operation		GB2423.1—89
3	High Temperature	Ta=+80℃, 240hrs	IEC60068-2-2,
	Storage (non-operation)		GB2423.2—89
4	Low Temperature	Ta=-30℃, 240hrs	IEC60068-2-1
	Storage (non-operation)		GB2423.1—89
5	High Temperature & High Humidity Operation	Ta = +60℃, 90% RH max,240 hours	Note2
			IEC60068-2-3,
			GB/T2423.3—2006
6	Thermal Shock (non-operation)	-30℃ 30 min~+80℃ 30 min,	Start with cold temperature, end
		Change time:5min, 100 Cycle	with high temperature
			IEC60068-2-14,GB2423.22—87
7	Electro Static Discharge (operation)	$\pm$ 2KV,Human Body Mode,100pF/1500 $\Omega$	IEC61000-4-2
			GB/T17626.2—1998
8		Sine Wave Frequency range:10~55Hz,	
	Vibration (non-operation)	Stroke:1.5mm	IEC60068-2-6
		Sweep:10Hz~55Hz~10Hz 2 hours for each	GB/T2423.10—1995
		direction of X.Y.Z.(6 hours for total)	
9	Shock (non-operation)	100G 6ms, ±X,±Y,±Z 3times for each	IEC60068-2-27
		direction	GB/T2423.5—1995
10	Package Drop Test	Height:80 cm, 1 corner, 3 edges, 6	IEC60068-2-32
		surfaces	GB/2423.8—1995
11	Package Vibration Test	Random Vibration:	IEC60068-2-34
		0.015G*G/Hz for 5-200Hz,	
		-6dB/Octave from 200-500Hz	
		2 hours for each direction of X,Y,Z	
		(6 hours for total)	

Note1: Ts is the temperature of panel's surface.

Note2: Ta is the ambient temperature of samples.

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# 10. MECHANICAL DRAWING



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# 11. Packing Drawing

**TBD** 

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## 12. Precautions for Use of LCD Modules

#### 12.1 Handling Precautions

- **12.1.1.** The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- **12.1.2**. If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- **12.1.3.** Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- **12.1.4.** The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- **12.1.5.** If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- **12.1.6.** Do not attempt to disassemble the LCD Module.
- **12.1.7**. If the logic circuit power is off, do not apply the input signals.
- **12.1.8.** To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
- a. Be sure to ground the body when handling the LCD Modules.
- b. Tools required for assembly, such as soldering irons, must be properly ground.
- c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
- d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

#### 12.2 Storage precautions

- **12.2.1.** When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- **12.2.2.** The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0°C ~ 40°C Relatively humidity: ≤80%

12.2.3. The LCD modules should be stored in the room without acid, alkali and harmful gas.

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## 12.3Transportation Precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

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